



Course Outcome	Bloom's K-level	Q. No.	<b>SECTION – B (5 X 5 = 25 Marks)</b> <b>Answer ALL Questions choosing either (a) or (b)</b>
CO1	K3	11a.	Illustrate the floating point representation. <b>(OR)</b>
CO1	K3	11b.	Determine the ASCII code for alphabets in capital and small.
CO2	K3	12a.	State Boolean laws and theorems. <b>(OR)</b>
CO2	K3	12b.	Discover the function of OR gate and its truth table.
CO3	K4	13a.	Describe the working of half-adder with neat logic circuit. <b>(OR)</b>
CO3	K4	13b.	Design a 1-to-4 demultiplexer and explain its operation.
CO4	K4	14a.	Describe the 4 bit serial-in-serial out shift register with neat logic diagram. <b>(OR)</b>
CO4	K4	14b.	Compare asynchronous counter with synchronous counter.
CO5	K5	15a.	Explain the difference between RAM and ROM. <b>(OR)</b>
CO5	K5	15b.	Discuss programmable logic array (PLA) and its applications.

Course Outcome	Bloom's K-level	Q. No.	<b>SECTION – C (5 X 8 = 40 Marks)</b> <b>Answer ALL Questions choosing either (a) or (b)</b>
CO1	K3	16a.	Perform the following conversion (i). $(A6F)_{16} = \text{-----} ( )_8?$ (ii). $(127)_{10} = \text{-----} ( )_2?$ (iii). $(3764)_8 = \text{-----} ( )_{16}?$ (iv). $(11101)_2 = \text{-----} ( )_{10}?$ <b>(OR)</b>
CO1	K3	16b.	Describe two types of Non-weighted codes.
CO2	K4	17a.	Illustrate the Sum of Product and Product of Sum method in detail. <b>(OR)</b>
CO2	K4	17b.	Show that NOR and NAND gates are universal gates.
CO3	K4	18a.	With neat logic circuit, explain the operation of full adder circuit with its truth table. <b>(OR)</b>
CO3	K4	18b.	Examine the function of parity generator /checker with neat logic circuit.
CO4	K5	19a.	Explain the operation of J-K flip-flop and master-slave J-K flip-flop. <b>(OR)</b>
CO4	K5	19b.	Explain the working of 4-bit ring counter with neat logic circuit.
CO5	K5	20a.	Classify the different types of ROM and explain in detail. <b>(OR)</b>
CO5	K5	20b.	Discuss in detail about field programmable logic array.